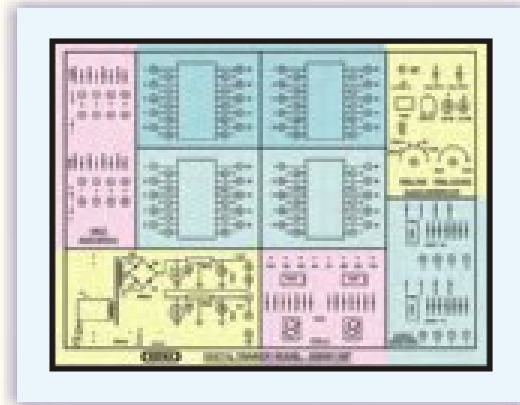




DIGITAL IC TRAINER

MODEL DICDM100P

This trainer has been designed with a view to verify truth table of logic gates and address and to construct small digital circuits and perform various experiments.



SPECIFICATIONS

- | | |
|----------------------------------|---|
| 1. Power supply | : 230V Ac, + 10 % single phase, 50Hz. |
| 2. Built in Power Supply | : 5 V, 500mA and +12V, 500mA regulated |
| 3. Clock generator | : Variable from 20Hz to 20KHz |
| 4. High /Low switches | : 8 Nos provided to apply 0 and 1 level |
| 5. Logic Level Indicator LEDs | : 8 Nos. provided for output /input level indication. |
| 6. Zip sockets | : 20 Pin - 4 Nos, 40 Pin - 1 No for inserting ICs |
| 7. Seven segment Display Decoder | : 2 Nos. |
| 8. Standard Accessories | : A Instructional Manual |

EXPERIMENTS

1. To demonstrate the operation of TTL Inverter (NOT Gate).
2. To demonstrate the operation of 2 Input TTL NAND Gate.
3. To demonstrate the operation of two input TTL AND Gate.
4. To demonstrate the operations of two input TTL NOR Gate.
5. To study the operation of two input TTL OR GATE.
6. To demonstrate the operation of 2 Input TTL Exclusive OR GATE.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-